Residual stress analysis of sputtered Tantalum Silicide thin film

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ABSTRACT

RESIDUAL STRESS ANALYSIS OF SPUTTERED TANTALUM SILICIDE THIN FILM

by
Young Joo Song

The influence of different argon pressures on the residual stress, microstructure, and resistivity of sputtered tantalum silicide thin films has been investigated. TaSi2 films were deposited onto Si wafers by dc magnetron sputtering. The deposition temperature was assumed to be 300°C. The thickness was about 0.5µm. The residual stresses in the films deposited at different argon pressures were determined by curvature measurement method. The compressive intrinsic stress of 1033.4MPa was measured at PAr = 0.5 mTorr. The intrinsic stress in the films seemed to change from compression to tension as PAr increased above 8mTorr. A maximum tensile intrinsic stress of 221MPa was obtained at 10 mTorr and the tensile intrinsic stress decreased at the higher argon pressure. SEM showed an accompanying microstructural change from a dense structure at the low pressures to an open growth structure with some gaps between grains at the highest pressure. The electrical resistivity exhibited a sputtering-pressure dependence and seemed to be closely related to the microstructure of films.
RESIDUAL STRESS ANALYSIS OF
SPUTTERED TANTALUM SILICIDE THIN FILM

by
Young Joo Song

A Thesis
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Mijoeng and Grace
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CHAPTER 1

INTRODUCTION

Micromechanical sensors are devices which measure physical quantities such as force, pressure, and acceleration through the deformation and/or resulting stresses in suitably designed microstructures. Most silicon based sensors are fabricated from silicon substrates using thin film deposition, patterning, and selective etching of films. However, since these films are often subjected to large mechanical stresses resulting from the specific deposition conditions, temperature changes during processing and in service, and from the presence of passivation coating, there has now developed a strong appreciation that an understanding of mechanical properties is essential for improving the reliability and lifetime of thin films. Thus, it has become progressively more important to understand the non-electrical properties of thin film materials for use in actuators, sensors, and even VLSI devices.

Recently, the examination of residual stresses in thin films has progressed to the study of the mechanical properties of microelectronic structures and a broad range of thin film materials [1]. These residual stresses cause great problems in not only the fabrication of micromechanical devices, such as wrinkling in thin diaphragms and deflection in cantilevers but also the fabrication of VLSI devices, such as pattern deformation, passivation crack and chip fracture. In these applications, the prediction and the control of stresses are critical because they affect the performance of the devices and even their feasibilities.

The goal of this work is to investigate the residual stresses in sputtered TaSi$_2$ thin films. Since it has been known that the magnetron-sputtered thin films display a rich variety of effects, including compressive-to-tensile stress transitions
as a function of a working gas pressure [2], TaSi2 thin films were deposited on Si substrates at various Ar pressures (deposition from magnetron sources gives access to a wide range of low working gas pressures with minimal substrate heating). After deposition, the residual stresses in the films were measured by curvature measurement method. Finally, SEM micrographs of the films will show that residual stresses correlate directly with microstructural features and physical properties.

Tantalum silicide thin film was chosen as a specimen for several reasons. It is a low resistance material compared to polysilicon (approximately an order of magnitude lower). It has a very high electromigration activation energy, which is 1.7 eV [3] (this is almost three times higher than aluminum of which electromigration activation energy is about 0.6 eV), and this eliminates the reliability problem caused by electromigration. Tantalum silicide is compatible with standard IC processing techniques. In addition, the microhardness of TaSi2 is significantly higher than that of many other silicides.

Chapter 2 describes a fundamental of residual stress in thin films. The origins and types of stresses are investigated and a quantitative calculation of film stress, Stoney formula, is introduced. Finally, the several technologies for stress measurement are discussed.

Chapter 3 examines the properties of tantalum silicide. For this, section 3.1 describes an overview of metal silicides including the reason why the metal silicides have been getting attention of scientists for both VLSI and micromechanical applications. Section 3.2 introduces the methods of formation of silicides and section 3.6 illustrates the properties of tantalum silicide.

Chapter 4 explains the properties of a magnetron sputtering and the microstructure of sputtered films. In section 4.3, an overview of the stresses in magnetron-sputtered thin film is also presented.
Chapter 5 describes the experimental procedures, such as sputter-deposition and stress measurements in detail. In chapter 6, the experimental results, discussions, and challenges are presented. Finally, chapter 7 gives a conclusion of the thesis.

All sputtering depositions were performed at the NJIT Microelectronics Research Center (NJIT MRC) and all measurements were done at NJIT's Thin Film Characterization Lab and NJIT's Optical Imaging Lab.
CHAPTER 2

FUNDAMENTALS OF RESIDUAL STRESSES IN THIN FILMS

2.1 Origins of Thin Film Stresses

In addition to externally applied stresses, incidental stresses induced during the manufacturing process and during use play significant role in determining the mechanical behavior of thin films. Changes in important physical and chemical properties of the films, adhesion failure, and cracking of the film or substrate may be caused by internal film stress. Some of these negative effects are exaggerated when the film is subjected to additional external loading, particularly when the substrate is flexible or the film is largely unsupported.

Vacuum deposition of thin film on substrates almost always results in internal film stresses, which fall into two basic categories; thermal and intrinsic stress. Thermal stresses are due to a mismatch of thermal expansion coefficients between film and substrate, and intrinsic stresses are the result of defects accumulated during deposition or growth. Thermal stresses usually develop in thin films when high temperature deposition or high temperature annealing are involved, and are usually unavoidable. To see how mismatch of film and substrate thermal expansion coefficients generates film stresses, imagine a semi-infinite film deposited on semi-infinite substrate at high temperature. The film is several orders of magnitude thinner than the substrate (the thin film approximation). At this temperature, the thin film is in a stress-free state. Next, imagine detaching the film from the substrate and cooling the system to room temperature (Figure 2.1). Usually, the substrate dimensions undergo minor shrinkage in the plane while the film dimensions are more greatly reduced. In order to reapply the film to the substrate with complete surface coverage, the film must be stretched with a biaxial
Figure 2.1  Thermally induced biaxial stress in a thin film

1. film & substrate are stress free at high temperature

2. detach film and cool, causing relatively large film contraction

3. apply tensile stress to film to match substrate dimensions

4. bond film to substrate residual tensile stress remains
tensile load. This "thought experiment" is analogous to the development of
tensile thermal stresses in thin film attached to substrate.

Intrinsic stresses are due to structural or compositional changes that result
in film volume changes. Densification may be due to phase transformation,
precipitation, dislocation movement, excess vacancy annihilation, and grain
boundary relaxation. Film are sometimes far from thermodynamic equilibrium
during deposition. Densification results in stresses that are present in the absence
of thermal effects (Figure 2.2). Epitaxial growth stresses may also be present, the
result of slight lattice mismatch between film and substrate.

Residual stresses must be superimposed on thermal and intrinsic stresses
caused by assembly and normal operation of microelectronic devices. Sensors and
actuators are intentionally subjected to additional thermal and mechanical loading
in the performance of the functions for which they were designed. Also,
particularly in soft and low melting point materials like aluminum, residual stresses
tend to relax through diffusion. Flaws such as voids and hillocks may form,
creating weak points where failure is likely. Thus, an understanding of all sources
of stress is critical for complete understanding of thin film mechanical properties
(Figure 2.3).

2.2 Tensile and Compressive Stress in Film
Thin films can be stressed even without the application of external loading and are
said to possess residual stresses. Residual stresses are, of course, not restricted to
composite film-substrate structures, but occur universally in all classes of
homogeneous materials under special circumstances. A state of nonuniform plastic
deformation is required, and this frequently occurs during mechanical or thermal
processing. For example, when a metal strip is reduced slightly by rolling between
cylindrical rolls, the surface fibers are extended more than the interior bulk.
Grains 1 and 2 before growth. Atoms in grain #1 are heavily shaded.

Growth of grain #1 has caused a volume change. The film must have tensile stress.

**Figure 2.2** Grain growth induced stress in a thin film
Figure 2.3 Parameters that affect thin film stress [4]
Machining a thin surface layer from the rolled metal will upset the mechanical equilibrium and cause the remaining material to bow.

Residual stresses arise in casting, welds, machined and ground materials, and heat-treated glass. The presence of residual stresses is usually undesirable, but there are cases where they are beneficial. Tempered glass and spot-peened metal surfaces rely on residual compressive stresses to counteract harmful tensile stresses applied in service. A model for generation of residual stress during the deposition of films is illustrated in Figure 2.4. Regardless of the stress distribution that prevails, maintenance of mechanical equilibrium requires that the net force $F$ and bending moment $M$ vanish on the film-substrate cross section. Thus,

$$ F = \int \sigma \, dA = 0 \quad (2.1) $$

$$ M = \int \sigma \, y \, dA = 0 \quad (2.2) $$

where $A$ is the sectional area and $y$ is the moment lever arm. Intuitive use will be made of these basic equations, and they are applied analytically in deriving the Stoney formula in the next section. In the first type of behavior shown in Figure 2.4 (a), the growing film initially shrinks relative to the substrate. Surface tension forces are one reason why this might happen; the misfit accompanying epitaxial growth is another. Compatibility, however, requires that both the film and substrate have the same length. Therefore, the film is constrained and stretches, and the substrate accordingly contracts. The tensile forces developed in the film are balanced by the compressive forces in the substrate.

However, the combination is still not in mechanical equilibrium because of the uncompensated end moments. If the film-substrate pair is not restrained from moving, it will elastically bend as shown, to counteract the unbalanced moments.
Figure 2.4 Sequence of events leading to (a) residual tensile stress in film; (b) residual compressive stress in film
Thus, films containing residual tensile stresses bend the substrate concavely upward. In an entirely similar fashion, compressive stresses develop in films that tend to initially expand relative to substrate (Figure 2.4 (b)). Residual compressive film stresses, therefore, bend the substrate convexly outward. These relations are perfectly general regardless of the specific mechanisms that cause films to stretch or shrink relative to substrates. Sometimes the tensile stresses are sufficiently large to cause film fracture. Similarly, excessive high compressive stresses can cause film wrinkling and local loss of adhesion to the substrate.

2.3 The Stoney Formula

We now turn our attention to a quantitative calculation of film stress as a function of substrate bending. The formulas that have been used in virtually all experimental determinations of film stress are variants of an equation first given by Stoney in 1909 [5]. This equation can be derived with reference to Figure 2.5, which shows a composite film-substrate combination of width $w$. The film thickness and Young's modulus are $d_f$ and $E_f$, respectively, and the corresponding substrate values are $d_s$ and $E_s$. Due to lattice misfit, differential thermal expansion, film growth effects, etc., mismatch forces arise at film-substrate interface. In the free-body diagrams of Figure 2.5 (b) each set of interfacial forces can be replaced by the statically equivalent combination of a force and moment : $F_f$ and $M_f$ in the film, $F_s$ and $M_s$ in the substrate, where $F_f = F_s$. Force $F_f$ can be imagined to act uniformly over the film cross section ($d_f w$) giving rise to the film stress. The moments are responsible for the bowing of the film-substrate composite. Equation (2.2) requires equality of the clockwise and counterclockwise moments, a condition expressed by

$$((d_f + d_s)/2) F_f = M_f + M_s$$

(2.3)
Consider now an isolated beam bent by moment \( M \), as indicated in Figure 2.5 (c). In this case, the deformation is assumed to consist entirely of the extension or contraction of longitudinal beam fibers by an amount proportional to their distance from the central or neutral axis, which remains unstrained in the process. The stress distribution reflects this by varying linearly across the section from maximum tension \((+\sigma_m)\) to maximum compression \((-\sigma_m)\) at the outer beam fibers. In terms of the beam radius of curvature \( R \), and angle \( \theta \) subtended, Hooke's law yields

\[
\sigma_m = E \left\{ \frac{(R \pm d/2) \theta - R \theta}{R \theta} \right\} = \pm \frac{E d}{2R} \quad (2.4)
\]

Corresponding to this stress distribution is the bending moment across the beam section:

\[
M = 2 \int_0^{d/2} \sigma_m w \left( \frac{y}{d/2} \right) y dy = \frac{\sigma_m d^2 w}{6} = \frac{E d^3 w}{12R} \quad (2.5)
\]

By extension of this result, we have

\[
M_f = E_f d^3 w / 12 R \quad \text{and} \quad M_s = E_s d^3 w / 12 R \quad (2.6)
\]

Lastly, in order to account for biaxial stress conditions, it is necessary to replace \( E_f \) by \( E_f / (1 - \nu) \), and similarly for \( E_s \). Substitution of these terms in Equation (2.3) yields
Figure 2.5  Stress analysis of film-substrate combination; (a) composite structure (b) free-body diagrams of the film and substrate with indicated interfacial forces and end moments (c) elastic bending of beam under applied end moment
Since $d_1$ is normally much larger than $d_f$, the film stress $\sigma_f$ is, to a good approximation, given by

$$\sigma_f = \frac{E_f}{v_f} = \frac{l}{6R} \frac{E_s d s^3}{(1 - v_s) d_f}$$

(2.8)

Equation (2.8) is the Stoney formula. Values of $\sigma_f$ are determined through measurement of $R$.

2.4 Stress Measurement Techniques

Three basic methods exist for measuring the stresses of the thin films on the substrates: curvature measurement, x-ray diffraction, and photoelastic method.

2.4.1 Curvature (Bending) Measurement Method

Curvature measurement is based on bending forces exerted by thin films on substrate. This method is used for both in-situ and post-processing measurements of residual stresses. Several mechanical property measuring methods are also based on this effect. A biaxial stress in a thin film on a substrate will cause the substrate to elastically deform. The biaxial stress produces shear stresses on the film / substrate interface near the edges of the film. Bending deformation is due to these shear stresses. If the thin-film approximation is made, only the biaxial modulus of the substrate must be considered in evaluating the bending due to biaxial stresses. No thin film properties must be assumed. Film stress is directly proportional to displacement.
For in-situ measurement of the film stresses during film deposition, cantilever beams are commonly employed [6]. Beam-tip deflection can be measured by a variety of techniques. In-situ measurement is complicated by the need to consider other bending mechanisms such as momentum transfer from the deposition process [7] and temperature gradients [8]. After deposition, secondary bending sources no longer require consideration, but complications arise from geometric deviations from beam theory found in microcantilever beams.

A disc-shaped substrate is most often used for post-processing stress measurements. This technique measures average biaxial stress in a film over a relatively large area and avoids some of the geometric complications of microcantilever measurements. Optical measurement methods such as interferometry and laser rastering, and grazing incidence x-ray scattering (GIXS) techniques are used.

Interferometry determines curvature from the interference between reflections from the substrate and a flat surface [9]. Laser rastering measures the deflection of a laser beam reflected from the substrate surface [10] (Figure 2.6). These methods measure average stress in plane and through thickness through the properly designed equipments.

2.4.2 X-Ray Diffraction Method
When a polycrystalline piece of metal is deformed elastically in such a manner that the strain is uniform over relatively large distances, the lattice plane spacings in the constituent grains change from their stress-free value to some new value corresponding to the magnitude of the applied stress, this new spacing being essentially constant from one grain to another for any particular set of planes similarly oriented with respect to the stress. This uniform microstrain causes a shift of the diffraction lines to new $2\theta$ positions. On the other hand, if the metal is
Figure 2.6 Laser measurement of wafer curvature; If the sample is warped, the extent of the reflected image is an indication of its radius of curvature.
deformed plastically, the lattice planes usually become distorted in such way that the spacing of any particular \((hkl)\) set varies from one grain to another or from one part of a grain to another. This nonuniform microstrain causes a broadening of the corresponding diffraction line. Actually, both kinds of strain are usually superimposed in plastically deformed metals, and diffraction lines are both shifted and broadened, because not only do the plane spacings vary from grain to grain but their mean value differs from that of the undeformed metal.

X-ray method is nondestructive for measurement of surface stress. If the stress is to be measured at some point below the surface, material must be removed down to that point to expose a new surface for x-ray examination; the x-ray method then becomes destructive. Ordinarily, however, one is most interested in the stress at the surface, where the applied stress is usually highest and where failures usually originate.

The x-ray method has the great advantage that it makes possible repeated measurements on the same specimen. For example, we may measure stress before and after some treatments designed to produce or modify various stages in its service life. Note also that the x-ray method measure the existing stress, whether it be solely residual or the sum of residual and applied. It therefore has the capability measuring the actual service stress in a machine or structure under a service load. Note, however, that stress is not measured directly by the x-ray method but by calculation or calibration. In principle, the x-ray method is applicable to any polycrystalline material. While it has had some application to stress measurement in ceramic and rocks, its major use is the measurement of residual stress in metals and alloys.

Since the angular position \(2\theta\) of the diffracted beam is measured directly with a diffractometer, it is convenient to write the stress equation in terms of \(2\theta\) rather than plane spacings. Equation (2.9) allows us to calculate the stress in any
chosen direction from plane spacing obtained from two measurements made in a plane normal to the surface and containing the direction of the stress to be measured. (Figure 2.7)

\[
\sigma_s = \frac{E}{(1 + \nu) \sin^2 \psi} \left( \frac{d_i - d_n}{d_n} \right) \quad (2.9)
\]

Differentiating the Bragg's law \( \lambda = 2d \sin \theta \), we obtain

\[
\frac{\Delta d}{d} = -\frac{\cot \theta \Delta 2\theta}{2} \quad (2.10)
\]

Combining this relation with Equation (2.9) gives

\[
\sigma_s = \frac{E \cot \theta (2\theta_n - 2\theta_i)}{2 (1 + \nu) \sin^2 \psi} \quad (2.11)
\]

Put

\[
K_i = \frac{E \cot \theta}{2 (1 + \nu) \sin^2 \psi} \quad (2.12)
\]

Then

\[
\sigma_s = K_i (2\theta_n - 2\theta_i) = K_i (\Delta 2\theta) \quad (2.13)
\]

where \( 2\theta_n \) is the observed value (in radians) of the diffractometer angle in the "normal" measurement \( (\psi = 0) \) and \( 2\theta_i \) is the value in the inclined measurement \( (\psi = \psi) \). Figure 2.8 shows the use of diffractometer for stress measurement. the constant \( K_i \) is called the stress constant. For greatest sensitivity \( K_i \) should be as large as possible.

The x-ray method has been reviewed by Barrett and Massalski [11], Klug
Figure 2.7  (a) Plane-spacing diagram; (b) and (c) Orientations of x-ray beams relative to specimen (Ns = normal to specimen surface, Np = normal to reflecting planes)
Figure 2.8 Use of a diffractometer for stress measurement

**Figure 2.9** Infrared photoelastic apparatus
and Alexander [12], Taylor [13], and Norton [14,15]. A detailed treatment has been published by the Society of Automotive Engineers [16].

2.4.3 Photoelastic Method

When a beam of polarized light passes through an elastically stressed sample which consists of transparent isotropic material, the beam decomposed into two rays polarized in the planes of principal stresses in the sample. Therefore, interference patterns will develop which are proportional to the difference in the principal stresses. A setup for the photoelastic measurements are shown in Figure 2.9 [17].
CHAPTER 3

INVESTIGATION OF TANTALUM SILICIDE

3.1 Background of Silicide
The primary thrust of very large scale integration (VLSI) has been the miniaturization of devices to increase packing density, achieve higher speed and consume less power [18]. The continued evolution of smaller and smaller devices has aroused a renewed interest in the development of new metallization schemes for low resistivity gates, interconnection, and ohmic contacts. This interest in new metallization schemes has been aroused by the fact that with the scaling down of the device sizes, the line width gets narrower and sheet resistance contributing to RC delay increases.

Aluminum and its alloys have been used extensively for metallization in integrated circuits because of their low resistivities (i.e., 2.7 µΩ-cm for Al and up to 3.5 µΩ-cm for its alloys). However, the use of aluminum in integrated circuits creates problems, such as junction spiking and electromigration [19]. It is also required that all post gate deposition processing of the devices to be limited to very low temperatures preferably below 500°C. Aluminum on silicon leads to metallurgical interactions causing serious instabilities. Annealing of aluminum on silicon (typically at 450°C) causes dissolution of silicon by diffusion into the metal and leads to pit formation [20]. The pits in silicon become visible after selectively removing Al from the surface. In actual devices, diffused aluminum is present in silicon below the Al / Si interface. If the penetration is deep, it leads to contact and junction failure. Electromigration causes considerable material transports in Al because of the enhanced and directional mobility of atoms caused by electron momentum transfer. Aluminum gathers in the direction of electron flow, leading
to a discontinuity in the current-carrying lines. This type of device failure seriously affects the reliability of silicon devices [21].

Instead of metal, polysilicon can be used as a gate material and/or an interconnection. The structure of polysilicon is strongly influenced by dopants, impurities, deposition temperature and post deposition cycles [22]. Films deposited for temperatures below 575°C, are amorphous with no detectable structure. Polysilicon deposited above 625°C has a columnar structure, which leads to higher resistivity. The undoped polysilicon films can be highly resistive (400-700 Ω-cm). However, their resistivity can be decreased by doping them with appropriate dopant gases introduced during the deposition process. The major disadvantage of polysilicon as a gate material is its high resistivity. With the increasing complexity and density of ICs, the width and spacing of the polysilicon runners have to be reduced. With desired improvements in speed-power product, the RC delay introduced by the polysilicon films becomes unacceptable.

Refractory metals were investigated for applications in VLSI technology in the 1970's. The two most commonly investigated materials were tungsten and molybdenum [23,24]. These materials have much higher melting points than silicon and their thermal expansion coefficients are similar to that of silicon. But the use of refractory metals requires complete passivation of these metals from oxidizing ambient and they may form silicides at temperatures above 900°C when they are in contact with silicon. The formation of silicides is accompanied by a volume shrinkage which can result in development of cracks. These cracks pose functional and reliability problems in ICs. Among many problems of refractory metals are those of cleanliness, contact to other conductors, and deposition and etching of the metals for pattern generation. The uncertainties associated with the stability of these metal films have led to search for alternative materials.

The silicides have attracted attention because of their low resistivities and
high temperature stability. The use of silicides, with resistivity values one tenth those of polysilicon, is certain to improve the speed of ICs. Expected higher electromigration resistance and the possibility of forming silicides directly on polysilicon, thus preserving the basic polysilicon MOS gate while decreasing the resistance, make these silicides attractive for gate and interconnect metallization [25,26]. Table 3.1 [27] shows the comparison of properties of the silicides of Ti, Ta, Mo, and W.

3.2 Formation of Silicide

Silicides can be formed by, basically, four techniques, each of which involve a metal deposition followed by a thermal step to form the silicide:

1. Direct metallurgical reaction: deposition of the pure metal on silicon (i.e. onto single crystal or polycrystalline silicon)
2. Co-evaporation: Simultaneous evaporation of the silicon and the refractory metal from two sources
3. Chemical Vapor Deposition: Formation of a thin film on Si by the reaction of vapor phase chemicals (reactants) that have the required constituents
4. Sputtering: Sputter deposition of the silicide, either from a composite target, or by cosputtering or layering

3.2.1 Direct Metallurgical Reaction

In this technique, a refractory metal film is deposited directly on a silicon surface, and the wafer is annealed at relatively low temperatures. Metal silicides are initially formed and continue to grow until the metal is consumed [28]. Typically at this point, the next metal rich phase begins to grow. For example, Figure 3.1 shows the effect of temperature on the formation of WSi₂ and W₅Si₃. W₅Si₃ has
Table 3.1 Comparison of the properties of silicides of Ti, Ta, Mo and W

<table>
<thead>
<tr>
<th>Property</th>
<th>TiSi₂</th>
<th>TaSi₂</th>
<th>MoSi₂</th>
<th>WSi₂</th>
</tr>
</thead>
<tbody>
<tr>
<td>Resistivity (µ Ω·cm)</td>
<td>≈ 25</td>
<td>≈ 50–55</td>
<td>≈ 90–100</td>
<td>≈ 70</td>
</tr>
</tbody>
</table>

Formed by co-deposition\(^a\) of metal and Si followed by sintering

<table>
<thead>
<tr>
<th>Etch rate in (10:1)BHF (Å/min)</th>
<th>≥2000</th>
<th>100–300</th>
<th>small</th>
<th>small</th>
</tr>
</thead>
</table>

Dry etching in
- (a) Plasma
- (b) RSE

<table>
<thead>
<tr>
<th>Oxidation in</th>
<th></th>
<th></th>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>(a) Dry O₂</td>
<td>Yes</td>
<td>No (Yes)(^b)</td>
<td>Yes</td>
<td>Yes</td>
</tr>
<tr>
<td>(b) Wet O₂</td>
<td>Yes</td>
<td>Yes</td>
<td>Yes</td>
<td>Yes</td>
</tr>
</tbody>
</table>

Stability of the metal oxide
- Good
- Good
- Poor, vaporizes
- Poor, vaporizes

Oxidation Stability of silicide on
- (a) silicon
- (b) oxide

<table>
<thead>
<tr>
<th>Al-silicide interaction temperature (°C)</th>
<th>&gt;500</th>
<th>&gt;500</th>
<th>&gt;500</th>
<th>&gt;500</th>
</tr>
</thead>
</table>

Recommended for use
- (a) on oxide
- (b) on silicon

<table>
<thead>
<tr>
<th></th>
<th>Yes</th>
<th>Yes(^c)</th>
<th>No</th>
<th>No</th>
</tr>
</thead>
</table>

\(^a\) Cosputtering, sputtering from sintered target or coevaporation

\(^b\) No for Ta sintered on polysilicon to form silicide and yes for cosputtered silicide on polysilicon

\(^c\) Not to be exposed to oxidizing ambients at high temperature
Figure 3.1 Phase diagram of tungsten-silicon system
has the highest melting point and WSi$_2$ has the least. This temperature dependence has been observed for most silicide formations and is an indication of a diffusion limited process. In this WSi$_2$ reaction, for each angstrom of tungsten thickness consumed, 2.53 Å of silicon are consumed resulting in 2.58Å of WSi$_2$ being formed. An important care must be taken so that the sufficient silicon should be available when using this technique.

3.2.2 Co-Evaporation

The evaporation method utilizes the simultaneous of the metal and Si under high vacuum conditions. The metal and Si can be vaporized by electron beam, rf induction, laser, or resistive heating. Since the refractory metals like W, Ta, Ti, and Mo have very high melting points (1670 °C to 2996 °C) and silicon has a low vapor pressure, e-beam evaporation can be chosen as a best choice. In e-beam evaporation a stream of electrons is accelerated to high kinetic energy and the beam is directed at the material to be evaporated and the kinetic energy is transformed to thermal energy upon impact. As the material is held at high temperature, this electron stream can melt and evaporate any material provided that the beam can supply an energy to the evaporant at an equal rate at which heat is lost. Electron beam guns are built to supply up to 1200KW of highly concentrated electron beam power for evaporation applications. Very high film deposition rates can thereby be obtained as a result of high power available. Typically two guns are employed with separate power supplies. Careful determination of the evaporation rate as a function of power for both metal and Si must be determined. The correct amount of power is supplied to each gun to provide the proper M/Si ratio, where M denotes metal. X-ray damage from e-beam evaporation is generally annealed out during the high temperature sinter operation. A typical configuration for deposition is shown in Figure 3.2.
Figure 3.2 Typical arrangement of electron-beam evaporator
1. of 270°C electron gun 2. centrally relatively to the planetary substrate holder 3.
3.2.3 Chemical Vapor Deposition (CVD)

In this technique, the material which forms a thin film on the substrate is produced by the chemical reaction that occurs on the substrate. The reaction is generally induced by heat, which causes either a decomposition of vapor or a reaction between different gaseous species in the ambient. Such decompositions are limited only by the availability of the reactants that are in the gas phase or that will easily vaporize. The reactants must decompose or react at temperatures that are low enough so that they do not affect the characteristics of the processed substrates.

Tungsten and molybdenum which are common metals for forming the silicides have been very successfully deposited using LPCVD (low-pressure chemical vapor deposition). The CVD for WSi$_2$ is generally performed in either a hot-wall low pressure system or a cold-wall low pressure system such as shown in Figure 3.3.

3.2.4 Sputtering

In the technique of sputtering using one composite target, the sputtering chamber has a single sputtering target. The Si:M ratio in the film is tied to that of the target and only small variations can be effected by changing the sputtering parameters. Figure 3.4 shows the Si:Ta ratio of the Ta-Si films deposited using various sputtering pressures for the same sputtering target [29]. Thus, to obtain significant Si:M ratio variations, one must have several targets with Si:M ratios spanning the desired range.

In the technique of sputtering using two or more targets, the sputtering chamber can have two or more independently powered sputtering targets. In this case, the deposition rate is established from the individual metal and silicon targets for a given condition of sputtering. These rates are used to obtain the desired Si:M atomic ratio in the deposited silicide film.
Figure 3.3  Schematic of the cold-wall reactor for CVD of WSi$_2$
Figure 3.4 Si:Ta ratio as a function of sputtering pressure on films obtained with target Si:Ta ratio of 2.6
3.2.5 Advantages and Disadvantages of Each of the Techniques

The technique of direct metallurgical reaction discussed in section 3.2.1 has one main advantage. The silicide growth from this method usually have a lower resistivity when compared to the other methods. Both polycide and silicide structures can be formed using this technique and the selective etching is possible. The disadvantage of this technique is that M/Si ratio which depends on the phase formed can not be easily controlled. The silicide formed from this technique is sensitive to sintering enviroment and after sintering, the surface formed can be rough.

The co-evaporation method have the following advantages. The purity of the silicide formed is relatively good and the sintering enviroment is not critical when compared to the other techniques. Moreover, the surface formed after sintering is quite smooth. However, the control of M/Si ratio is very difficult and step coverage is poor.

CVD method offers several advantages, the most important of which are excellent step coverage and high throughput, higher purity films (low O\textsubscript{2} content), and improved uniformity on large area silicon wafers. Possible poor adhesion is the disadvantage.

Sputtering has these advantages: improved step coverage, good sintering enviroment, good step coverage, and trustworthy control of M/Si ratio. Among the various sputtering techniques, the sputtering from a composite target is a promising technology and has been extensively used for deposition of molybdenum silicide and tungsten silicide and polycide gates in VLSI because high quality films can be deposited with high throughput. The disadvantages are the contamination from a target.
3.3 Properties of Tantalum Silicide

The recent interests in micro electromechanical systems necessitated a closer look at the transition-metal silicides, including to their thermodynamics, electrical and mechanical properties, and their stability at very high temperatures. Among the transition-metal silicides, tantalum silicide has very attractive electrical and micromechanical characteristics. It is a low resistance material compared with polysilicon (approximately an order of magnitude lower). It has a very high electromigration activation energy which is 1.7 eV [3] (this is almost three times higher than aluminum of which electromigration activation energy is about 0.6 eV), which this eliminates the reliability problem caused by electromigration. It is comparable with standard IC processing techniques, and the hardness of TaSi2 is significantly higher than that of many other silicides. Listed below are some of the characteristics of the tantalum silicide which make it favorable for the micro sensors and actuators applications:

(1) TaSi2 has become a standard material in MOS processing technology [30]
(2) Good adhesion to silicon, silicon nitride and oxide
(3) Low resistivity : 35 - 55 µΩ-cm
(4) No reaction with aluminum up to 500°C
(5) As hard as steel : hardness = 1402 kg/mm² (Table 3.2)
(6) Thermally stable : TCE = 8.8 ppm/°C (Table 3.2)
(7) Insoluble in H2SO4 + H2O2 mixture
(8) Good etch resistance in HF, primary wet etchant of SiO2 (i.e. sacrificial layer)
(9) Very high electromigration activation energy : 1.7 eV
(10) Hard to form a native oxide layer [31]

Table 3.2 also shows that melting points of tantalum silicide is higher than any other silicides. Table 3.3 reveals the crystal structure of the tantalum silicide family [32].
### Table 3.2 Basic constants of silicides and elements

<table>
<thead>
<tr>
<th>Material</th>
<th>Thermal expansion coefficient (ppm/°C)</th>
<th>Mirohardness (kg/mm²)</th>
<th>Melting point (°C)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Si</td>
<td>3</td>
<td>850</td>
<td>1420</td>
</tr>
<tr>
<td>Ti</td>
<td>8.5</td>
<td>775</td>
<td>1661±10</td>
</tr>
<tr>
<td>Ta</td>
<td>6.5</td>
<td>935</td>
<td>2997</td>
</tr>
<tr>
<td>Mo</td>
<td>5</td>
<td>890</td>
<td>2617</td>
</tr>
<tr>
<td>W</td>
<td>4.5</td>
<td>860</td>
<td>3411</td>
</tr>
<tr>
<td>TiSi₂</td>
<td>12.5</td>
<td>892</td>
<td>1540</td>
</tr>
<tr>
<td>TaSi₂</td>
<td>8.8-10.7</td>
<td>1402</td>
<td>2200±100</td>
</tr>
<tr>
<td>MoSi₂</td>
<td>8.25</td>
<td>1200</td>
<td>1980</td>
</tr>
<tr>
<td>WSi₂</td>
<td>6.25-7.9</td>
<td>1074</td>
<td>2165</td>
</tr>
</tbody>
</table>
Table 3.3  Crystal structure of the tantalum silicide family

<table>
<thead>
<tr>
<th>Silicide</th>
<th>Structure</th>
<th>Lattice constant (Å)</th>
<th>Density (g/cm²)</th>
</tr>
</thead>
<tbody>
<tr>
<td>TaSi₂</td>
<td>Hexagonal</td>
<td>a = 4.7821, c = 6.5695</td>
<td>9.08</td>
</tr>
<tr>
<td>Ta₂Si</td>
<td>Teragonal</td>
<td>a = 6.175, c = 5.039</td>
<td>13.544</td>
</tr>
<tr>
<td>Ta₃Si₃</td>
<td>Teragonal</td>
<td>a = 9.88, c = 5.06</td>
<td>13.401</td>
</tr>
</tbody>
</table>
CHAPTER 4

INVESTIGATION OF THE MICROSTRUCTURE OF MAGNETRON-SPUTTERED THIN FILM

4.1 Magnetron Sputtering

4.1.1 What is Sputtering

Sputtering has been widely used as a metallization technique in VLSI. Sputtering is a physical phenomenon involving the acceleration of ions, usually Ar⁺, through a potential gradient, and the bombardment by these ions of a "target" or cathode. Through momentum transfer, atoms near the surface of the target material become volatile and are transported as a vapor to the substrates. At the substrates, the film grows through deposition. Conventional sputtering has found wide application in IC processing. Metals such as Al, Ti, Pt, Au, Mo, W, Ni, and Co are readily sputtered using either a dc or rf discharge in a diode system. A high electron density is required in the discharge to increase the ion current density at the target surface and thus prevent the oxide from forming. The high density may be achieved by introducing an auxiliary discharge, as in triode sputtering, or through the use of magnetic fields to capture the electrons and increase their ionizing efficiency, as is done in magnetron sputtering.

Some characteristics of sputter deposition are: (1) the ability to deposit alloy films with composition similar to that of the target, (2) the incorporation of Ar(~2%) and background gas(~1%) in the film, and (3) in conventional diode systems, considerable heating of the substrates (~350°C) by the secondary electrons emitted by the target. Often rf energy may be applied to the substrates which cause them to be bombarded by ions. If the rf is applied prior to metal deposition, the process is termed "sputter etching". Sputter etching may clear residual films from
window areas and enhance the contact between the metal and substrate. If the negative bias is applied to substrate during film deposition, it is a bias-sputter deposition, and may enhance the step coverage of the film or reduce the severity of the surface topography.

4.1.2 Various Effects of Sputtering

When a solid surface is bombarded by energetic ions, numerous effects may occur: (1) emission of neutral (sputtered) particles; (2) emission of secondary electrons; (3) emission of positive and/or negative ions; (4) emission of electromagnetic radiation; (5) reflection of incident particles; (6) desorption of gases; (7) implantation of incident particles; (8) heating; (9) chemical dissociation; (10) bulk diffusion; (11) crystallographic changes; and (12) reflection of some emitted species back to the target surface either by collision in the gas discharge of electrostatic attraction. Since the target is held at high negative potential, secondary electrons are emitted at target potential and accelerated toward the substrate. Upon arrival, such energy as they retain after colliding with sputtering gas atoms is liberated at the substrate in the form of heat.

It is highly improbable that a positive target ion generated at the target surface could escape the negative field. Honig[33] has shown that the fraction of positive ions originating at the target surface that escape the target field is very small (<1%) and the escape mechanism is very complex. The number of positive ions that are produced and, hence, can escape, is larger for compounds than for monatomic targets. Benninghoven[34] has shown that this occurs because of dissociation reactions in which the primary bombardment breaks down molecules into positive and negative ions that are then sputtered. He showed that the number of positive and negative ions released from metal targets decreases to a negligible minimum as the surface oxides on the target are sputtered away. Negative ions
produced by dissociation reactions are repelled from the target and accelerated toward the substrate. The ions may be neutralized by stripping collisions in the gas.

We have noted above that numerous energetic particles (electrons, ions, and neutrals) are liberated from the target and propelled toward the substrates. If bombardment by these particles is undesirable, high gas pressure can aid in slowing these particles to minimize substrate heating and other effects. In some cases, it is desirable to maximize the bombardment, thus dictating the use of low gas pressure.

It was shown above that the likelihood of a positive target ion escaping the target field is very small. However, it is entirely possible to create positive target ions by collisions in the rf glow discharge. This is less a problem in dc- than rf-glow discharges because of the higher ionization probability in the latter.

The probability that ions so produced would be neutralized before arriving at the substrate is very small. Positive ions and electrons recombine more readily at solid surfaces than in the gas discharge because both momentum and energy can be conserved with greater ease in a three-body collision (ion, electron, and surface) than in a two-body collision (ion and electron). Hence ions of target material produced in this manner may bombard the substrate surface, or in some cases, return to the target to cause self-sputtering.

4.1.3 Properties of Magnetron Sputtering
A typical magnetron sputtering process shown in Figure 4.1 has a shaped and closed path magnetic field to trap and concentrate the electrons produced in the discharge at the target surface. The electrons are confined to the space near the target. The high density cloud of electrons promotes ionization of the sputtering gas in the region close to the target surface, and the target is negatively biased to attract ions to the target. The high energy impact of these ions on the target dislodges atoms from the material which are collected on the substrate surface. Due to the
Figure 4.1 Basic operation of magnetron sputtering system
high ionization efficiency of this process, low power levels may be used, and at the same time high deposition rates are achieved. Because electron leakage is restricted by the magnetic field, electron bombardment of the substrate is minimized and heating of the growing film and substrate is substantially reduced.

The magnetron sputtering process has many advantages [35,36]. The primary advantages are:
(1) high deposition rate,
(2) ease of sputtering any metal, alloy, or compound,
(3) high-purity of the deposited films,
(4) good adhesion of films,
(5) excellent coverage of steps and small features,
(6) ability to coat heat sensitive substrates,
(7) ease of automation, and,
(8) excellent uniformity on large area substrates. Magnetron sputtering, because of the above advantages, is a very powerful technique which can be used in a wide range of applications.

4.2 Microstructure of Sputter-Deposited Films
4.2.1 Columnar Grain Structure
The columnar grain structure of thin films has been a subject of interest for several decades. This microstructure consisting of a network of low-density material surrounding an array of parallel rod shaped columns of higher density has been much studied by transmission and scanning electron microscopy. Columnar structures grow when the mobility of deposited atoms is limited. They have been observed in high-melting point materials (Cr, Be, Si, and Ge), in compounds of high binding energy (TiC, TiN, CaF₂, and PbS), and in non-noble metals evaporated in the presence of oxygen (Fe and Fe-Ni). Amorphous films of Si, Ge,
SiO, and rare earth-transition metal alloys (Gd-Co), whose very existence depends on limited adatom mobility, are frequently columnar when deposited at sufficiently low temperature. Magnetic, optical, electrical, mechanical, and surface properties of films are affected, sometimes strongly, by columnar structures.

4.2.2 Zone Models for Sputtered Films

An essential feature of thin films is that they are formed from a flux that approaches the substrate from a limited set of directions. Furthermore, at low homologous temperature $T$, relative to coating material melting point $T_m$, a growth structure defined by columnar voided boundaries is superimposed on the intrinsic grain structure as shown in Figure 4.2(a). Thus thin film microstructures tend to have an anisotropic character which influences such properties as their magnetic anisotropy and their effectiveness for lateral current transport and as diffusion barriers. For example, the grain or growth boundaries present preferred diffusion paths which often extend through the entire coating thickness. (Activation energies for surface, grain boundary, and bulk diffusion are typically in the ratio 1:2:4 so that at $T/T_m < 0.5$ surface and grain boundary diffusion rates can be orders of magnitude larger than bulk diffusion rates.)

It is useful to envision coating growth as proceeding in three general steps as indicated schematically in Figure 4.2(b). The first step involves the transport of the coating species to the substrate. The second step involves the adsorption of these species onto the surface of the substrate or growing coating, their diffusion over this surface, and finally their incorporation into the coating or their removal from the surface by evaporation or sputtering. The third step involves movement of the coating atoms to their final position within the coating by processes such as bulk diffusion. In the case of sputter deposition, the transport step is controlled by parameters such as the apparatus geometry and working gas pressure, while the
Figure 4.2 Schematic illustrations showing (a) the superposition of growth and microstructures for films deposited under conditions of low adatom mobility (b) the steps involved in the condensation of a vapor during film growth.
diffusion steps are controlled largely by the substrate temperature, but may be
significantly influenced by energetic particle bombardment.

Structure-zone models have proven very useful in providing an overview of
the relationship between the microstructure of vacuum deposited coatings and the
most prominent deposition parameters. The first such model was published by
Movchan and Demchisshin (MD) in 1969. MD found that the microstructures of
thick (0.3 to 2 mm) evaporated coatings of Ti, Ni, W, ZrO and Al₂O₃ could be
represented as a function of $T/T_m$ by three zones, each with its own characteristic
structure and physical properties. The low temperature ($T/T_m < 0.3$) zone 1
structure was columnar, consisting of tapered units defined by voided growth
boundaries of the type shown in Figure 4.2(a). The zone 2 structure ($0.3 < T < 0.5$)
consisted of columnar grains, which were defined by metallurgical grain boundaries
and increased in width with $T/T_m$ in accordance with activation energies typical of
surface diffusion. The high temperature zone 3 ($T/T_m > 0.5$) structure consisted of
equiaxed grains, which increased in size in accordance with activation energies
typical of bulk diffusion.

In 1973 the zone model was extended to magnetron sputtered metal films
by adding an axis to account for the effect of the Ar working gas pressure as shown
in Figure 4.3 [37,38]. The model was based on an examination of 0.025 to 0.25 mm
thick coatings of Ti, Cr, Fe, Cu, Mo, and Al alloy deposited onto glass substrates
using magnetron sputtering sources. In this model, the MD zones were associated
with conditions where the physics of coating growth was dominated in turn by
mechanisms associated with each of the three steps shown in Figure 4.2 (b)-zone 1,
atomic shadowing during transport; zone 2, surface diffusion; and zone 3, bulk
diffusion. A fourth zone consisting of a dense array of poorly defined fibrous grains
was identified in the region between zone 1 and 2 and termed zone T since it was
believed to be a transition state between the two MD zones.
Figure 4.3 Microstructure zone diagram for metal films deposited by magnetron sputtering. 
T is the substrate temperature and Tm is the coating material melting point.
4.3 Stresses in Magnetron-Sputtered Thin Films

The results of extensive studies by Hoffman and Thornton [39, 40, 41] on magnetron-sputtered metal films are particularly instructive since the internal stress correlates directly with microstructural features and physical properties. Magnetron sputtering sources have made it possible to deposit films over a wide range of pressures and deposition rates in the absence of plasma bombardment and substrate heating. It was found that two distinct regimes, separated by a relatively sharp boundary, exist where the change in film properties almost discontinuous (Figure 4.4 and 4.5). The transition boundary can be thought of as a multidimensional space of the material and processing variables involved. On one side of the boundary, the films contain compressive residual stresses and entrapped gases, but exhibit near-bulklike values of electrical resistivity and optical reflectance. This side of the boundary occurs at low sputtering pressures, with high mass targets, and low deposition rates. On the other hand, elevated sputtering pressures, light target metals, and oblique incident angle of the depositing flux favor the generation of films possessing tensile stresses containing lesser amounts of entrapped gases.

Comparison with the microstructure of sputtered films reveals that elevated working pressures are conducive to development of columnar grains with intercrystalline voids. Such a structure exhibits high resistivity, low optical reflectivity, and tensile stresses. At lower pressures the development of the columnar structure with voids is suppressed. Energetic particle bombardment, mainly by sputtered atoms, apparently induces compressive film stress by an atomic peening mechanism.
Figure 4.4 Biaxial residual stresses as a function of Ar pressure for Cr, Mo, Ta, and Pt films sputtered onto silicon substrate.
**Figure 4.5** Argon transition pressure vs atomic mass of sputtered metals for tensile to compressive stress reversal
CHAPTER 5

EXPERIMENT

5.1 Wafer Cleaning and Sputter Deposition

Four silicon substrates were piranha cleaned (P-clean, \(5:1\) \(\text{H}_2\text{SO}_4 : \text{H}_2\text{O}_2\)) for 10 minutes at \(110^\circ\text{C}\) to remove any organic residues on the wafers. Then the wafers were rinsed in hot deionized (DI) water for 10 minutes followed by cold DI water rinse for 5 minutes. In addition, the silicon wafers were dipped into a 100:1 \(\text{H}_2\text{O}:\text{HF}\) mixture immediately before they were loaded into the sputtering chamber to etch off the native oxide after the acid cleaning.

A magnetron-type dc sputtering source with a composite target (Varian 3125 magnetron sputtering system) was used to deposit tantalum silicide thin films. The sputter chamber was pumped to a vacuum of less than \(8 \times 10^{-7}\) Torr before being back filled with argon gas. Four different deposition pressure (0.5, 3, 10, and 15 mTorr) were selected to see the stress transition because the stress transition was expected to occur in this range [40]. Tantalum silicide thin films were deposited on 4-inch diameter silicon wafers, 525 µm thick. The thickness of the silicide film was measured by a surface profilometer. The films deposited had approximately same thickness (0.5 ± 0.05 µm). The deposition temperature was measured by a thermocouple and its probe was placed within one inch above the substrate for an accurate measurement of the substrate surface temperature (in a conventional sputtering system, a temperature sensor is normally attached to the substrate holder.). Deposition parameters are given in Table 5.1.
Table 5.1 Deposition parameters for tantalum silicide

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Steady State Substrate Temperature</td>
<td>300°C</td>
</tr>
<tr>
<td>Base Pressure</td>
<td>$8.0 \times 10^{-7}$ Torr</td>
</tr>
<tr>
<td>Deposition Pressures</td>
<td>0.5, 3, 10 and 15 mTorr</td>
</tr>
<tr>
<td>Backfill Gas</td>
<td>Argon</td>
</tr>
<tr>
<td>Deposition Rate</td>
<td>2-4 Å/second</td>
</tr>
<tr>
<td>Target Composition</td>
<td>TaSi$_{2.4}$</td>
</tr>
</tbody>
</table>
5.2 Measurements

Stress in the tantalum silicide films was calculated from wafer curvature measured by laser beam method shown in Figure 5.1. Two circular spots were produced from the two laser beams and were projected on substrate/film by a mirror, then the reflection from the surface of the wafer was projected again on the screen with scale by the mirror. The distance of the two spots on the screen reflected from the mirror was recorded before and after deposition. Finally, the average stresses of films were determined by the Stoney's formula.

Film resistivity were measured by the four-point probe technique. A very convenient way to measure the resistivity of a film is to lightly press a four-point metal-tip probe assembly into the surface as shown in Figure 5.2. The outer probes are connected to the current source, and the inner probes detect the voltage drop. Electrostatic analysis of the electric potential and field distributions within the film yields \( \rho = KVI^{-1}d \), where \( d \) is a film thickness and \( K \) is a constant dependent on the configuration and spacing of the contacts. In my experiment, \( K = \pi/\ln2 = 4.53 \) was chosen because the film is large in extent (100mm) compared with the probe assembly and the probe spacing (2.5mm) is large compared with film thickness (0.5µm) [41]. Table 5.2 shows the measured parameters.
Table 5.2 Measured Parameters

<table>
<thead>
<tr>
<th>$P_{Ar}$ (mTorr)</th>
<th>$\Delta$ before</th>
<th>$\Delta$ after</th>
<th>$\sigma$ (MPa)</th>
<th>$I$ (mA)</th>
<th>$V$ (mV)</th>
<th>$\rho$ (µΩ-cm)</th>
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<tr>
<td>0.5</td>
<td>106</td>
<td>135</td>
<td>-713.4</td>
<td>0.833</td>
<td>0.30</td>
<td>81.6</td>
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<td>1.666</td>
<td>0.71</td>
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<td>2.500</td>
<td>1.13</td>
<td>102.4</td>
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<td>1.56</td>
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<td>4.165</td>
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<td>0.833</td>
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<td>5.000</td>
<td>6.97</td>
<td>315.7</td>
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</table>

* where, - and + denote compressive and tensile stress, respectively.
stress(\rho) = 12.3 \times (\Delta_{\text{before}} - \Delta_{\text{after}}) / t_f

**Figure 5.1** Optical system for stress measurement in NJIT's Thin Film Characterization Laboratory

**Figure 5.2** Four-point probe method for resistivity measurement for thin films in NJIT's Optical Imaging Laboratory
CHAPTER 6

DISCUSSION

The deposited tantalum silicide thin films might have thermal stresses because films prepared at elevated temperatures and then cooled to room temperature would be thermally stressed. Assuming a biaxial stress state, the thermal residual stress contribution to the total measured stress can be established as \( \sigma_r = \Delta \alpha \Delta T [E_d/(1-\nu_d)] \) [42], where \( \Delta \alpha \) is the difference in the thermal expansion coefficients between film and substrate, \( \Delta T \) is the temperature change after deposition (The steady state temperatures of substrates and films were assumed to be 300°C although the film temperature was expected to be slightly higher.) and \([E_d(1-\nu_d)]\) is the biaxial elastic modulus of the TaSi\(_2\) film. For \( \Delta \alpha = 5.8 \times 10^{-6} \), \( E_r = 158 \text{GPa} \), and \( \nu_r = 0.213 \) [43], the estimated thermal stress in these films is 320MPa. This value is less than the maximum residual tensile stress (541MPa) measured at 10mTorr. Therefore, the residual stress behavior of the films seemed to be dominated by an intrinsic stress arising from the incomplete structural ordering during film growth. Consequently, the stress analysis will be focused on the intrinsic stress behavior.

The intrinsic stress of the sputtered tantalum silicide thin films could be changed from highly compressive to highly tensile by varying the argon pressure. Figure 6.1 shows the thermal, intrinsic and measured residual stress of the films as a function of the argon pressure. It could be seen that the intrinsic stress was quite sensitive to the argon pressure. The intrinsic stress of the film deposited at the lowest working gas pressure (0.5mTorr) revealed maximum compressive (1033.4MPa). The transition pressure at which the intrinsic stress changes from compression to tension was about 8mTorr. The maximum value for the tensile intrinsic stress (221MPa) was measured at 10mTorr. With further increase, the
Figure 6.1 Average residual stresses of tantalum silicide thin films at various $P_{Ar}$
Figure 6.2 Room temperature resistivity at various Ar pressure
Figure 6.3(a) Plan-view SEM micrograph of tantalum silicide thin film at 0.5mTorr
Figure 6.3(b) Plan-view SEM micrograph of tantalum silicide thin film at 3mTorr
Figure 6.3(c) Plan-view SEM micrograph of tantalum silicide thin film at 10mTorr
Figure 6.3(d) Plan-view SEM micrograph of tantalum silicide thin film at 15mTorr
tensile intrinsic stress of the film decreased to a value of 73.6MPa at 15mTorr. In the calculation of the stresses, Young's modulus E_s of 130 GPa and a Poisson ratio ν_s of 0.280 were used [44]. For convenience, the range of argon pressure in Figure 6.1 was divided into three regions, namely Region I (0.5 ~ 8mTorr), Region II (8 ~ 10mTorr) and Region III (10 ~15mTorr).

At lower pressures (Region I in Figure 6.1), compressive intrinsic stresses (-713.4 and -172.2MPa) were observed. The cause of the compressive stress has been studied well by Thornton [45,46,47]. Since higher particle energies are expected for lower sputtering pressures due to the fewer collisions (i.e., larger mean free path), the energetic particle bombardment of the film during deposition may be the major factor for a compressive stress [42,45]. The energetic particles may be sputtered atoms or working gas atoms which are neutralized and reflected at the cathode or a combination of both [48,49,50]. There may be several ways in which the energetic particle bombardment can promote the compressive stress in the films.

Energetic particle bombardment, mainly by sputtered atoms, apparently induces compressive film stress by the bombardment of the film resulting in the film compression. The compressive stress is also caused by the lattice expansion induced by the incorporated energetic working gas particles into films. In fact, when the energetic ions, which are accelerated through a potential gradient, bombard the target, some incident ions are reflected and neutralized at the target surface (If not neutralized at the target, it is highly improbable that the reflected positive ions at the surface could escape the negative potential) and carried to the substrate. The energy flux carried to the substrate by neutralized and reflected ions depends on the cathode shape (because of the scattering directions of the reflected species) and the working gas pressure. The SEMs show that the surface of the film
deposited at 0.5mTorr is very smooth (Figure 6.3(a)), but some defects are observed in the film at 3mTorr (Figure 6.3(b)).

At intermediate pressures (Region II in Figure 6.1), tensile intrinsic stresses (0 ~ 221MPa) were found. Furthermore, as the argon pressure increased, the stress also increased. Figure 6.3(c) shows the plan-view SEM micrograph of the film deposited at 10mTorr. The surface of the film deposited at 10mTorr was similar to that of the film deposited at 3mTorr, that is, the surface was not smooth and grains were not observed. Therefore, we could think that the argon pressure was still low enough to compress the growth structure through the energetic particle bombardment effects. Of course, we could also think that the mobility of sputtered atoms was high enough to fill in the gaps between grains, or the self-shadowing at grain edges was negligible.

At higher argon pressures (Region III in Figure 6.1), it was observed that as $P_{Ar}$ increased, the tensile intrinsic stress decreased to 73.6MPa. This phenomenon can be explained by the relaxation of the interforces acting between grains[51]. The presence of less dense regions during film growth may result in a tensile film stress due to the interforces which pull together. However, as the distance between the grains increases, the grains are no longer pull together and bring the relaxation of the tensile intrinsic stress. The SEM of the film at 15mTorr showed relatively large gaps between grains. However, although some gaps and grains were observed in this growth structure, the structure was an equiaxed structure rather than a complete columnar structure, that is, the diameter of grains was comparable to the thickness of the film. Therefore, we can think that the more columnar structure (i.e. more gaps) may be grown at higher argon pressure (because of the less energetic particle bombardment), or the more shadowing effect) or at lower temperature (because of the lower mobility of sputtered atoms).
Figure 6.2 shows the electrical resistivity of the films sputtered at various pressures. From the graph, we can see that as the argon pressure increases, the resistivity increases faster. The change of resistivity with respect to argon pressure seemed to be closely related to the growth structure of films. Thus, the sudden change of the increasing rate may be explained by the sudden change of growth structure. Unlike the intrinsic stress behavior, resistivity increased continuously although the slope changed suddenly into a higher value. Therefore, it may be concluded that the resistivity is affected by the microstructure of the film.
CHAPTER 7

CONCLUSION

(1) The stress versus argon pressure reveals the stress transition from compression to tension and tensile stress maximum.

(2) Tantalum silicide thin film can be prepared in any stress state (tensile, stress-free or compressive) by varying the argon pressure, i.e., the residual stress of tantalum silicide film is sensitive to the argon pressure.

(3) The cause of compressive residual stress can be explained by energetic particle bombardment at low gas pressures, i.e., the bombardment of films and the incorporation of working gas into films.

(4) At the highest pressure, the appearance of relatively large gaps between grains may dominate the stress behavior and cause the relaxation of tensile residual stress.

(5) The resistivity of the tantalum silicide thin film reveals a sputtering-pressure dependence and seems to be closely related to the microstructure of films.
REFERENCES


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